ISO-9001 CERTIFIED BY DSCC

SERIES

(315) 701-6751



DUAL OUTPUT RADIATION HARDENED HIGH EFFICIENCY, 9 AMP SWITCHING REGULATORS

4707 Dey Road Liverpool, N.Y. 13088

FEATURES:

- Up To 85% Efficiency
- 9 Amp Total Output Current
- 4.5V to 16V Input Range with Startup Bias
- 4.5V to 5.5V Input Range with VBias = VIN
- Preset 3.3V/2.5V, 3.3V/1.5V or 2.5/1.5V Output Voltage Combinations
- Custom Output Voltages Available
- 300KHz Switching Frequency
- · Hermetic Package with Three Lead Form Options
- -55°C to +125°C Operating Temperature Range
- 100K RAD Total Dose Rated

DESCRIPTION:

The MSK5047RH series are high efficiency, 9 amp, radiation hardened switching regulators. The output voltages are configured for 3.3V/2.5V, 3.3V/1.5V or 2.5V/1.5V internally with a tolerance of 1% at 4.0 amps. The operating frequency of the MSK5047RH is 300KHz. A low quiescent current and greater than 80% operating efficiency keep the total internal power dissipation of the MSK5047RH down to an absolute minimum. The device is packaged in a hermetic power package for high reliability applications, and is available fully screened to MIL-PRF-38534 Class H or K.

EQUIVALENT SCHEMATIC



TYPICAL APPLICATIONS

- Step-down Switching Regulator
- Microprocessor, FPGA Power Source
- High Efficiency Low Voltage Subsystem Power Supply

PIN-OUT INFORMATION

1	VIN	20	VOUT MAIN
2	VIN	19	VOUT MAIN
3	VIN	18	VOUT MAIN
4	VIN	17	RETURN
5	VIN	16	RETURN
6	VIN	15	RETURN
7	VBIAS	14	RETURN
8	CAP	13	VOUT AUX
9	ENABLE	12	VOUT AUX
10	CASE GND	11	VOUT AUX



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ABSOLUTE MAXIMUM RATINGS

Input Voltage	0.3V, +16V
Bias Voltage	0.3V, +5.5V
Enable	0.3V, 10.5V
Output Current	9.0 Amps Total
Thermal Resistance (@ 125°C)	9.0°C/W

Tst	Storage Temperature Bange		-65°C to	+150°C
131	Storage remperature name.			100 0

Tld (10 Seconds)

(10 0000103)
Case Operating Temperature
MSK5047RH Series
MSK5047RH K/H/E Series55°C to +125°C
Junction Temperature

ELECTRICAL SPECIFICATIONS

Parameter	Test Conditions ①	Group A	MSK 5047RH K/H/E			M\$K 5047RH			Unite
Falameter		Subgroup	Min.	Тур.	Max.	Min.	Тур.	Max.	Onits
VIN Input Supply Range (2)		1,2,3	4.5	-	16	4.5	-	16	V
VBias Input Supply Range 2		1,2,3	4.5	-	5.5	4.5	-	5.5	V
Rine Countrat	Startup	1,2,3	-	480	TBD	-	480	TBD	mA
Blas Current	Operating	1,2,3	-	1	TBD	-	1	TBD	mΑ
		1	1.48	1.50	1.52	1.46	1.50	1.54	V
Output Voltage 1.5VOOT (Aux)	\bigcirc	2,3	1.42	1.50	1.58	-	-	-	V
Output Voltage 2.5VOUT (Aux or Main) ⑦		1	2.47	2.5	2.55	2.45	2.5	2.55	V
		2,3	2.38	2.5	2.63	-	-	-	V
		1	3.27	3.3	3.33	3.23	3.3	3.37	V
Output Voltage 3.3VOOT (Main		2,3	3.14	3.3	3.47	-	-	-	V
Output Ripple		4	-	-	TBD	-	-	TBD	mVp-p
Output Current (Main) (2)	Within SOA	1	5.0	5.2	-	5.0	5.2	-	A
Output Current (Aux) 2	Within SOA	1	4.0	4.2	-	4.0	4.2	-	A
Land Regulation (Either Output)	1.0A <u><</u> IOUT <u><</u> 4.0A	1	-	0.5	1.0	-	0.5	1.5	%
Load Regulation (Either Output)		2,3	-	0.5	1.5	-	-	-	%
Line Regulation (Either Output)	VBias = 5V	1	-	0.5	1.0	-	0.5	1.5	%
Line Regulation (Either Output)	VIN Step=6V to 12V	2,3	-	0.5	1.5	-	-	-	%
Grane Regulation	1.0A <u><</u> IOUT <u><</u> 4.0A	1	-	0.5	1.0	-	0.5	1.0	%
Cross Regulation		2,3	-	0.5	1.5	-	-	-	%
Oscillator Frequency	IOUT <u>></u> 1.5A	4	270	300	330	270	300	330	KHz
	Open Circuit Voltage	1,2,3	TBD	4.5	TBD	TBD	4.5	TBD	V
Enable input voltage	Logic Low Disabled	1,2,3	TBD	4.0	TBD	TBD	4.0	TBD	V
Enable Input Current	VEN = OV	1	-	1	TBD	-	1	TBD	mA
Disabled Quiescent Current	VEN = OV	1,2,3	-	4	TBD	-	4	TBD	mA
	5047-2515RH	1	TBD	78	-	TBD	78	-	%
Efficiency	5047-3315RH	1	TBD	79	-	TBD	79	-	%
	5047-3325RH	1	TBD	81	-	TBD	81	-	%

NOTES:

- (1) $V_{\text{Bias}} = VIN = 5.0V$, IouT = 4.0A, $CIN = 100\mu$ F, $COUT = 200\mu$ F each output unless otherwise specified. (2) Guaranteed by design but not tested. Typical parameters are representative of actual device performance but are for reference only. (3) All output parameters are tested using a low duty cycle pulse to maintain TJ = Tc. (4) Industrial grade and 'E' suffix devices shall be tested to subgroup 1 unless otherwise specified.

- (a) industrial grade and 2 suffix devices shall be tested to subgroup 1 diffess otherwise specific filless otherwise specific fi

- Ø Alternate output voltages are available. Please contact the factory.

INPUT VBIAS:

The VBias pin of the MSK5047RH provides bias to the control circuitry for intial startup only. The Vbias pin can be connected directly to the input bus for 4.5V to 5.5V operation or it can be biased separately with a 4.5V to 5.5V source to extend the maximum input voltage of the device from 5.5V to 16V. The bias pin draws approximately 480mA during intial startup and drops down to approximately 1mA once the internal bootstrap takes over. The startup supply operates at approximately 175KHz for approximately TBD mS. After the bootstrap takes over the startup supply stops switching.

INPUT CAPACITOR SELECTION:

The MSK5047RH should have an external high frequency ceramic capacitor (0.1uF) between VIN and GND. Connect a low-ESR bulk capacitor directly to the input pin of the MSK5047RH. Select the bulk input filter capacitor according to input ripple-current requirements and voltage rating, rather than capacitor value. Electrolytic capacitors that have low enough ESR to meet the ripplecurrent requirement invariably have more than adequate capacitance values. Aluminum-electrolytic capacitors are preferred over tantalum types, which could cause power-up surge-current failure when connecting to robust AC adapters or low-impedance batteries. When operating the MSK5047RH with a separate bias supply the VBias pin requires TBD capacitance to ground. Additionally the MSK5047RH requires TBD μ F of capacitance on the cap pin.

OUTPUT CAPACITOR SELECTION:

The output capacitor values are generally determined by the ESR and voltage rating requirements rather than capacitance requirements for stability. Low ESR capacitors that meet the ESR requirement usually have more output capacitance than required for stability. Only specialized low-ESR capacitors intended for switching-regulator applications, such as AVX TPS, Sprague 595D, Sanyo OS-CON, Nichicon PL series or Kemet T510 series should be used.

The output ripple is usually dominated by the ESR of the filter capacitors and can be approximated as IRIPPLE x RESR. Including the capacitive term, the full equation for ripple in the continuous mode is VNOISE(p-p) = IRIPPLE x (RESR + 1/(2IIfC). The MSK5047RH can support up to TBD μ F capacitive load on each output.

ENABLE FUNCTION:

The enable function of the MSK5047RH is configured for open collector drive. Leaving the pin open allows normal operation. Pulling the pin low disables the device.

CAP PIN:

The cap pin of the MSK5047RH provides bulk storage and high frequency decoupling for the bootstrap supply. This pin requires 1μ F ceramic and TBD bulk capacitance for normal operation.

CROSS REGULATION:

TBD

DROPOUT VOLTAGE:

TBD

TYPICAL VBIAS = VIN APPLICATION



TBD

MECHANICAL SPECIFICATIONS



ORDERING INFORMATION



The above example is a +3.3V Main, +2.5V Aux, Military regulator with leads bent up.

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