RAD HARD POSITIVE 0.9A, LOW NOISE, LDO ADJ VOLTAGE REGULATOR

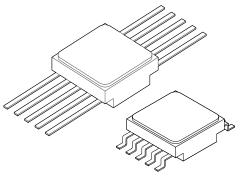
5965RH

FEATURES:

- TLINEAR Space Qualified RH1965 Die Manufactured using ______
- MIL-PRF-38534 Class K and H Processing & Screening
- Total Dose Hardened to 100 Krads(Si) (Method 1019.7 Condition A)
- Characterized to 300 Krads(Si) Total Dose

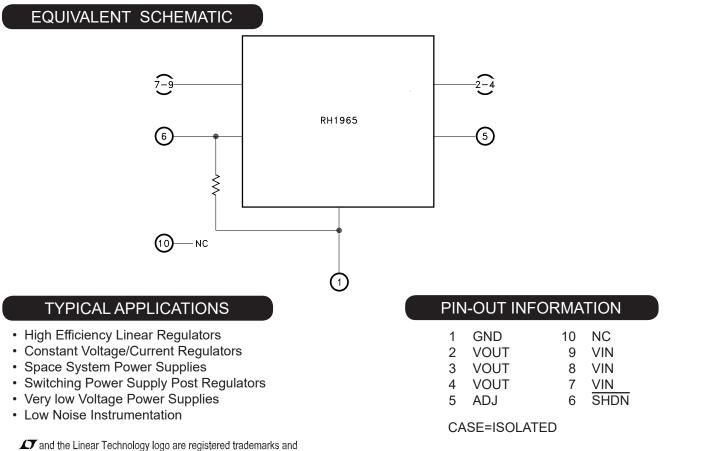
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- Low Dropout of 250mV at 0.5A
- Output Adjustable from 1.2V to 19.5V
- · Internal Short Circuit Current Limit
- Low Noise: 40uVRMS (10Hz to 100kHz)
- Output Current Capability < 0.9A
- · Internal Thermal Overload Protection
- Shutdown Pin Active Low
- · Available in Straight or Gull Wing Lead Form
- Contact MSK for MIL-PRF-38534 Qualification Status



DESCRIPTION:

The MSK5965RH is a radiation hardened low dropout adjustable linear regulator. This device features a low noise, adjustable output voltage range of 1.2V to 19.5V. The dropout voltage is typically 250mV with a 0.5A load. This, combined with the low ReJC, allows increased output current while providing exceptional device efficiency. Additionally, the MSK5965RH offers internal short circuit current and thermal limiting, which provides circuit protection and eliminates the need for excessive derating. The shutdown pin provides a method for sequencing the supply with minimal external components. The MSK5965RH is packaged in a hermetically sealed 10 pin flatpack with straight or gull wing leads and specifically designed for space/satellite applications.



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ABSOLUTE MAXIMUM RATINGS

Vin	Pin Voltage	±22V
Vout	Pin Voltage	±22V
	Input to Output Differential Voltage(1)	±22V
IOUT	Output Current	+0.9A
ADJ	Pin Voltage	±9V
SHDN	Pin Voltage	±22V

ELECTRICAL SPECIFICATIONS

Tst	Storage	Temperature	Range	65°C to +150°C	

- TLD Lead Temperature Range
- MSK5965RH.....-40°C to +85°C MSK5965K/HRH....-55°C to +125°C ESD Rating.....Class 1B

Parameter	Test Conditions (1) (11)			Group A	MSK5965RH			Units
Falameter				Subgroup	Min.	Тур.	Max.	Units
Minimum Input Voltage	VOUT = VADJ	ILOAD = 0.9A		1, 2, 3	-	-	2.3	V
	VIN = 2.1V, VOUT = VADJ	ILOAD = 1mA		1	1.182	-	1.218	
Adj Pin Voltage		Post Irradiation		2, 3	1.164	-	1.236	V
			FOST ITAUIATION	1	-	-	6	
Line Regulation	Δ VIN = 2.1V TO 20V, VOUT = VADJ	Iload = 1mA		2, 3	-	-	8	mV
			TO 0.04	1	-	-	8	
Load Regulation	VIN = 2.3V, VOUT = VADJ	$\Delta I_{LOAD} = 1 \text{mA}^2$		2, 3	-	-	16	mV
			Post Irradiation	1	-	-	12	
		ILOAD = 1mA		1 2, 3	-	-	80 140	mV
				2, 3	-	-	140	
		ILOAD = 100mA Post Irradiation		2.3	-	-	295	mV
	VIN = VOUT (NOMINAL) +1			1	-	-	190	
Dropout Voltage	\wedge VOUT = 1%			1	-	-	300	mV
	AV001 = 1%	Iload = 500mA		2, 3	-	-	430	
			Post Irradiation	1	-	-	320	
		ILOAD = 0.9A		2.3	-	-	435 600	mV
			Post Irradiation	2, 5	-	-	465	IIIV
	VIN = VOUT (NOMINAL) +1		Tr oot maanatom	1	-	-	1.1	mA
		ILOAD = 1mA		2, 3	-	-	1.5	mA
			1	-	-	4.6	mA	
0		ILOAD = 100mA Post Irradiation		<u>2, 3</u> 1	-	-		5.5 7
Gnd Pin Current (5)		ILOAD = 500mA	Post Irradiation	1	-	-	16.5	┟────┤
0			2.3	-	-	20	mA	
			Post Irradiation	1	-	-	25	
		ILOAD = 0.9A		1	-	-	30	mA
				2, 3	-	-	38	
			Post Irradiation	1	-	-	45	
Adj Pin Bias Current (6)	VOUT = V _{ADJ}			1	-	-	4.5	uA
	SHDN ≥ 1.5V, VOUT = ON			7 8a, 8b	-	-	-	P/F
Shutdown Pin Threshold			7	-	-	-	P/F	
	,			8a, 8b	-	-	-	
Shutdown Pin Bias Current	$\frac{VIN = 20V}{VIN = 0V}$			1	-	210	215	uA
Quiescent Current in Shutdown	VIN = 6V, VSHDN = 0V	0 1)/		1	-	-	1	uA
Current Limit	VIN = VOUT (NOMINAL) +1, \triangle VOUT = -	0.1V		1, 2, 3	0.9	-	-	A
Reverse-Output Current 7	VIN = 0V, VOUT = VADJ			1	-	-	400	uA
Thermal Resistance (2)	JUNCTION TO CASE @ 125°C			-	-	10.4	12.1	°C/W

(8) (9) (10)

NOTES:

- (1) U.O.S. VOUT= 2.5V, VIN= 6V, ILOAD= 1mA
- (2) Guaranteed by design but not tested. Typical parameters are representative of actual device performance but are for reference only. Not applicable to post irradiation performance.
- (3) Industrial grade devices shall be tested to subgroup 1 unless otherwise specified.
- (4) Subgroup 1, 7 TA = Tc = $+25^{\circ}$ C

2, 8a TA = Tc = +125°C

3, 8b TA = Tc = -55°C

(5) GND Pin current increases in Dropout.

- 6 ADJ pin bias current flows into the ADJ pin.
- (7) Reverse output current is tested with the VIN pin grounded and the VOUT pin forced to 1.2V. This current flows into the VOUT pin and out of the GND pin.
- (8) Absolute maximum input to output differential voltage is not achievable with all combinations of rated VIN pin and VOUT pin voltages. With the VIN pin at 22V, the VOUT pin may not be pulled below 0V. The total measured voltage from VIN to VOUT must not exceed ±22V
- (9) Maximum junction temperature limits operating conditions. The regulated output voltage specification does not apply for all possible combinations of input voltage and output current. Limit the output current range if operating at the maximum input voltage. Limit the input to output voltage differential if operating at the maximum output current.
- 1 Continuous operation at or above absolute maximum ratings may adversely affect the device performance and/or life cycle.
- (1) Pre and Post irradiation limits at 25°C, up to 100 Krad(Si) TID, are identical unless otherwise specified. Post irradiation performance is guaranteed by design with a 2X radiation design margin.

PIN FUNCTIONS

VIN - The VIN pins are the input supply pins for all of the internal circuitry. Decoupling capacitors are recommended to provide a low impedance source. All three pins must be connected for proper operation.

GND - Connect the bottom of the output voltage feedback divider directly to GND for optimum regulation. Connect the GND pin to the load ground trace outside of the primary power return path.

VOUT - The VOUT pins supply power to the load. A 10µF capacitor on the output with an ESR of less than 1 Ω is typically adequate to ensure stability. Applications with large output load transients require larger output capacitor value to minimize output voltage transients. All VOUT pins must be connected for proper operation.

ADJ - The Adjust pin is the input to the error amplifier. Connect to the center of the output voltage feedback divider. The ADJ pin voltage is 1.2V referenced to GND. It has a typical bias current of 1.3μ A that flows into the pin.

SHDN - Pulling the SHDN pin low puts the device into a low power state and turns the output off. Drive the SHDN pin with either logic or an open collector/drain with a pull-up resistor. If unused, connect the SHDN pin to VIN. The SHDN pin cannot be driven below GND unless it is tied to the VIN pin. If the SHDN pin is driven below GND while VIN is powered, the output will turn on. SHDN pin logic cannot be referenced to a negative supply rail.

TYPICAL APPLICATION CIRCUIT

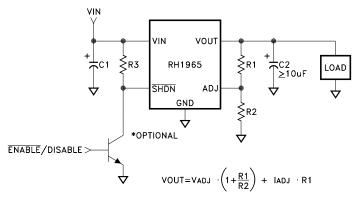


FIGURE 1

OUTPUT VOLTAGE SELECTION

As noted in the above typical applications circuit, the formula for output voltage selection is:

VOUT = VADJ X 1+ R1 = R2 + IADJ X R1

OVERLOAD RECOVERY

Like many IC power regulators, the MSK5965RH has safe operating area protection. The safe operating area protection decreases current limit as the input-to-output voltage increases and keeps the power transistor inside a safe operating region for all values of input-to-output voltage. The protective design provides some output current at all values of input-to-output voltage up to the device breakdown. When power is first applied, as input voltage rises, the output follows the input, allowing the regulator to start up into very heavy loads. During start-up, as the input voltage is rising, the input-to-output voltage differential is small, allowing the regulator to supply large output currents.

With a high input voltage, a problem can occur wherein removal of an output short will not allow the output to recover. Other regulators, such as the LT1083/LT1084/LT1085 family, also exhibit this phenomenon, so it is not unique to the MSK5965RH. The problem occurs with a heavy output load when the input voltage is high and the output voltage is low. Common situations occur immediately after the removal of a short-circuit or if the shutdown pin is pulled high after the input voltage has already been turned on. The load line for such a load may intersect the output current curve at two points. If this happens, there are two stable output operating points for the regulator. With this double intersection, the input power supply may need to be cycled down to zero and brought up again to make the output recover.

HEAT SINK SELECTION

To select a heat sink for the MSK5965RH, the following formula for convective heat flow may be used.

Governing Equation: TJ = PD x (R0JC + R0CS + R0SA) + TA

WHERE

TJ = Junction Temperature PD = Total Power Dissipation R0JC = Junction to Case Thermal Resistance R0CS = Case to Heat Sink Thermal Resistance R0SA = Heat Sink to Ambient Thermal Resistance TA = Ambient Temperature

Power Dissipation = (VIN-VOUT) x IOUT + (IGND x VIN)

Next, the user must select a maximum junction temperature. The absolute maximum allowable junction temperature is 150°C. The equation may now be rearranged to solve for the required heat sink to ambient thermal resistance (R0SA).

EXAMPLE:

An MSK5965RH is connected for VIN = +5V and VOUT = +3.3V. IOUT is a continuous 0.5A DC level and IGND=20mA. The ambient temperature is +25°C. The maximum desired junction temperature is +125°C. Power Dissipation = $(5V-3.3V)(0.5A) + (0.02A \times 5V)=0.95Watts$

 $R\theta JC = 12.08^{\circ}C/W$ and $R\theta CS = 0.15^{\circ}C/W$ for a most thermal grease.

Solve for R0SA:

$$\left[\frac{125^{\circ}\text{C} - 25^{\circ}\text{C}}{0.95\text{W}}\right] - 12.08^{\circ}\text{C/W} - 0.15^{\circ}\text{C/W} = 93.3^{\circ}\text{C/W}$$

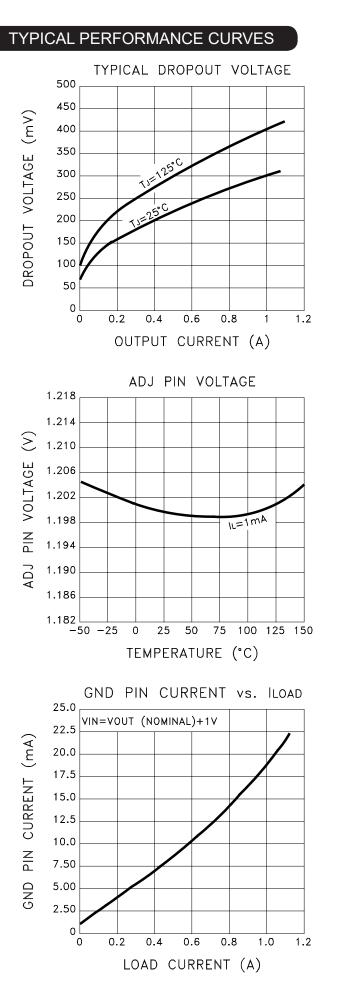
In this example, a heat sink with a thermal resistance of no more than 93.0°C/W must be used to maintain a junction temperature of no more than 125°C.

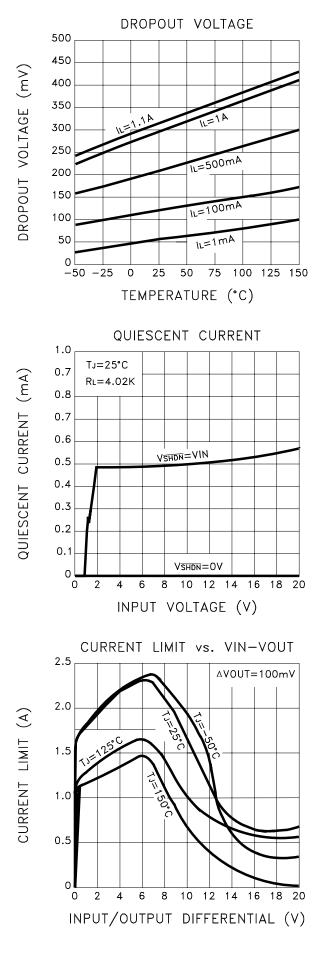
TOTAL DOES RADIATION TEST PERFORMANCE

Radiation performance curves for TID testing will be generated for all radiation testing performed by MSK. These curves show performance trends throughout the TID test process and can be located in the MSK5965RH radiation test report. The complete radiation test report will be available in the RAD HARD PRODUCTS section on the MSK website.

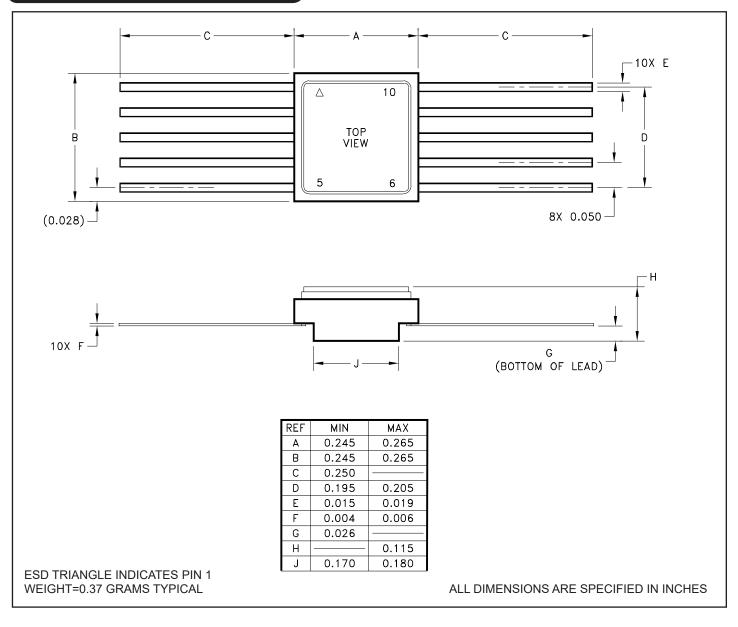
ADDITIONAL APPLICATION

For additional applications information, please reference Linear Technology Corporation's® LT1965 and RH1965 data sheets.

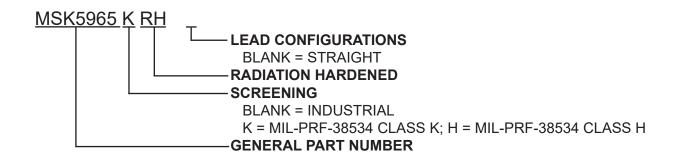




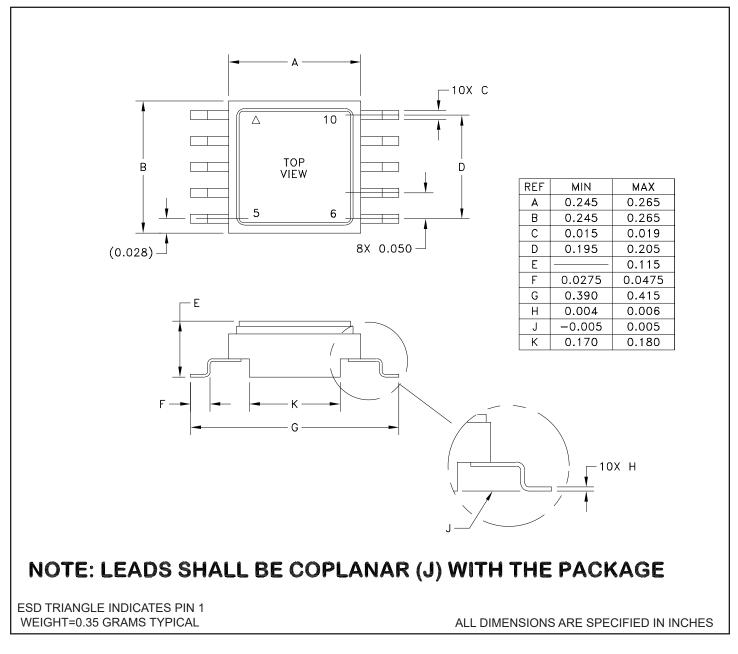
MECHANICAL SPECIFICATIONS



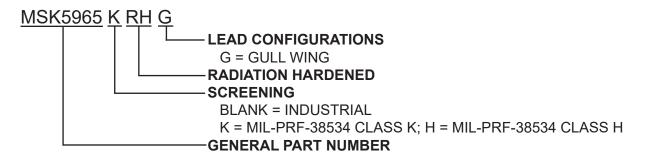
ORDERING INFORMATION



MECHANICAL SPECIFICATIONS



ORDERING INFORMATION



REVISION HISTORY

REV	STATUS	DATE	DESCRIPTION
A	Preliminary	05/15	Initial Release
В	Preliminary	09/15	Initial Release
С	Preliminary	09/16	Initial Release
D	Released	08/17	Update radiation performance, release preliminary status.
E	Released	11/18	Update mechanical specifications
F	Released	05/19	Correct typo in Electrical Table Parameter description - Shutdown Pin Threshold

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