

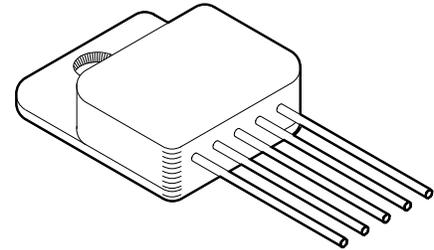


HIGH CURRENT, LOW DROPOUT VOLTAGE REGULATORS

5130 SERIES

FEATURES:

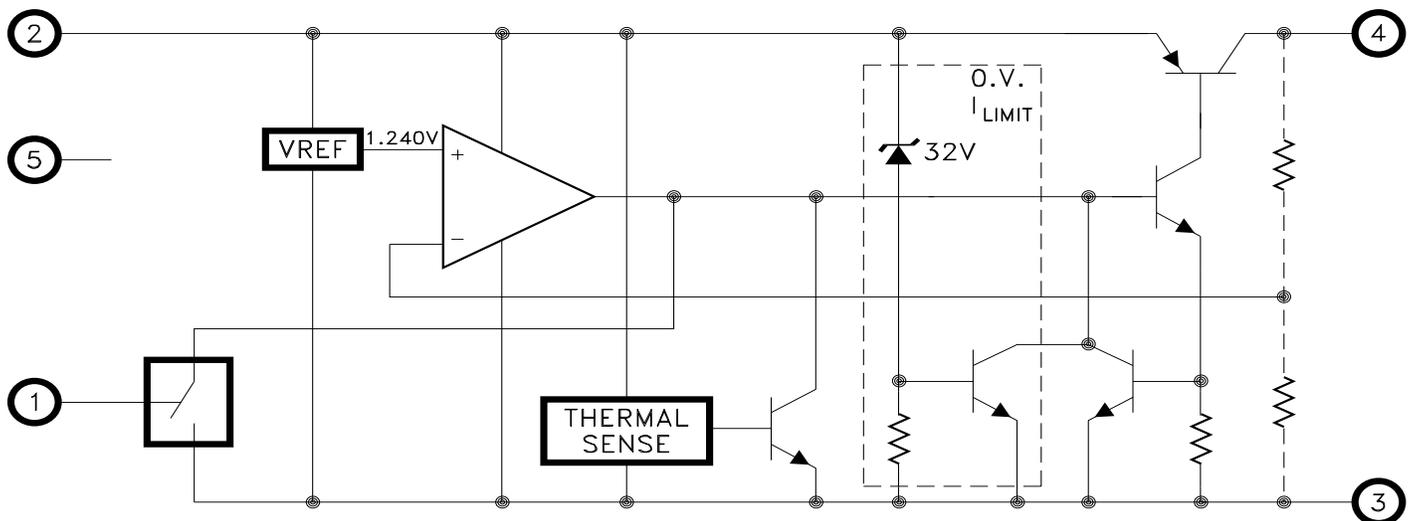
- Electrically Isolated Top Tab or Z Tab SIP
- Extremely Low Dropout Voltage: 350mV @ 3 Amps
- Available in 3.3V and Adjustable Versions
- Open Collector Error Flag Output
- TTL Level Enable Pin: Zero Current Shutdown Mode
- Reverse Battery and Load Dump Protection
- Low Ground Current: 32mA Typical at Full Load
- 1% Maximum Guaranteed Accuracy
- Output Current to 3 Amps
- See MSK5131 for Alternate Fixed Outputs



DESCRIPTION:

The MSK5130 series voltage regulators are available in +3.3V or adjustable output configurations. All boast ultra low dropout specifications due to the utilization of a super PNP output pass transistor with monolithic technology. Dropout voltages of 350mV at 3 amps are typical in this configuration, which drives efficiency up and power dissipation down. Accuracy is guaranteed with a 1% maximum output voltage tolerance. The series also offers a TTL/CMOS compatible on/off enable function as well as an output flag pin. The MSK5130 series is packaged in a space efficient 5 pin power SIP available in two styles with three lead bend options.

EQUIVALENT SCHEMATIC



Schematic shown for fixed output voltage versions

TYPICAL APPLICATIONS

- High Efficiency, High Current Linear Regulators
- Constant Voltage/Current Regulators
- System Power Supplies
- Switching Power Supply Post Regulators
- Battery Powered Equipment

PIN-OUT INFORMATION

MSK5130-00	MSK5130-3.3
1 ENABLE	1 ENABLE
2 VIN	2 VIN
3 GROUND	3 GROUND
4 VOUT	4 VOUT
5 ADJUST	5 FLAG

CASE=ISOLATED

ABSOLUTE MAXIMUM RATINGS ^⑫

V _{INP}	Input Voltage (100ms 1%D.C.)	-20V to +60V
V _{IN}	Input Voltage	26V
V _{EN}	Enable Voltage	-0.3V to 26V
I _{OUT}	Output Current	3.0A

T _{ST}	Storage Temperature Range	-65°C to +150°C
T _{LD}	Lead Temperature (10 Seconds Soldering)	300°C
T _J	Operating Temperature	MSK5130 Series: -40°C to +85°C MSK5130B Series: -55°C to +125°C

ELECTRICAL SPECIFICATIONS

Parameter	Test Conditions ^① ^③	Group A	MSK5130B SERIES			MSK5130 SERIES			Units
		Subgroup	Min.	Typ.	Max.	Min.	Typ.	Max.	
Output Voltage Tolerance ^⑨	I _{OUT} =10mA; V _{IN} =V _{OUT} +1V	1	-	±0.5	±1.0	-	±0.5	±1.0	%
		2,3	-	±1.0	±2.0	-	-	-	%
Dropout Voltage ^②	ΔV _{OUT} =-1%; I _{OUT} =100mA	1	-	80	175	-	80	200	mV
	ΔV _{OUT} =-1%; I _{OUT} =3A	1	-	350	600	-	350	625	mV
Load Regulation ^⑩	V _{IN} =V _{OUT} +1.5V	1	-	±0.2	±1.0	-	±0.2	±1.2	%
	10mA ≤ I _{OUT} ≤ 2.5A	2,3	-	±0.3	±2.0	-	±0.3	-	%
Line Regulation	(V _{OUT} +1V) ≤ V _{IN} ≤ 26V	1	-	±0.05	±0.5	-	±0.05	±0.6	%
	I _{OUT} =10mA	2,3	-	±0.5	±1.0	-	±0.5	-	%
Output Current Limit ^② ^⑪	V _{OUT} =0V; V _{IN} =V _{OUT} +1V	-	-	4.5	5.0	-	4.5	5.0	A
Ground Current ^②	V _{IN} =V _{OUT} +1V; I _{OUT} =1.5A	-	-	10	35	-	10	35	mA
	V _{IN} =V _{OUT} +1V; I _{OUT} =3A	-	-	32	-	-	32	-	mA
Output Noise ^②	C _L =20μF; 10Hz ≤ f ≤ 100KHz	-	-	400	-	-	400	-	μV
Enable Input Voltage ^②	HIGH/ON	1	2.4	1.2	-	2.4	1.2	-	V
	LOW/OFF	1	-	1.2	0.8	-	1.2	0.8	V
Enable Input Current ^②	HIGH/ON	1	-	20	600	-	20	600	μA
	LOW/OFF	1	-	-	2	-	-	2	μA
Shutdown Output Current ^②	V _{ENABLE} ≤ 0.8V	-	-	10	500	-	10	500	μA
Flag Output Leakage ^② ^⑨	V _{OH} =26V	-	-	0.01	2	-	0.01	2	μA
Flag Output On Voltage ^⑨	I _{OL} ≤ 250μA; V _{IN} =V _{OUT} -2V	1	-	0.2	0.4	-	0.2	0.4	V
Flag Threshold ^② ^⑨	V _{IN} =V _{OUT} -7%	-	-	75	-	-	75	-	mV
Reference Voltage ^⑧	Normal Operation	1	1.22	1.24	1.26	1.22	1.24	1.26	V
		2,3	1.21	-	1.27	1.21	-	1.27	V
Reference Voltage Temp Drift ^② ^⑧	Normal Operation	-	-	20	-	-	20	-	ppm/°C
Adjust Pin Bias Current ^② ^⑧	Full Temp; V _{IN} =V _{OUT} +1V	-	-	40	120	-	40	150	nA
Thermal Resistance ^②	Junction to Case @ 125°C	-	-	2.1	2.4	-	2.1	2.5	°C/W
Thermal Shutdown ^②	T _J	-	-	135	-	-	135	-	°C

NOTES:

- ① Output decoupled to ground using 28μF minimum capacitance unless otherwise specified.
- ② This parameter is guaranteed by design but need not be tested.
- ③ Typical parameters are representative of actual device performance but are for reference only.
- ④ All output parameters are tested using a low duty cycle pulse to maintain T_J = T_C.
- ⑤ Industrial grade devices shall be tested to subgroup 1 unless otherwise specified.
- ⑥ Military grade devices ('B' suffix) shall be 100% tested to subgroups 1, 2 and 3.
- ⑥ Subgroup 1 T_C=+25°C
Subgroup 2 T_J=+125°C
Subgroup 3 T_A=-55°C
- ⑦ Please consult the factory if alternate output voltages are required.
- ⑧ Applies to MSK5130-00 adjustable version only.
- ⑨ Applies to fixed output devices only.
- ⑩ Due to current limit, maximum output current may not be available at all values of V_{IN}-V_{OUT} and temperatures. See typical performance curves for clarification.
- ⑪ The output current limit function provides protection from transient overloads but it may exceed the maximum continuous rating. Continuous operation in current limit may damage the device.
- ⑫ Continuous operation at or above absolute maximum ratings may adversely effect the device performance and/or life cycle.
- ⑬ Internal solder reflow temperature is 180°C, do not exceed.

PART NUMBER	OUTPUT VOLTAGE
MSK5130-00	Adjustable
MSK5130-3.3	+3.3V

APPLICATION NOTES

REGULATOR PROTECTION:

The MSK5130 series is fully protected against reversed input polarity, overcurrent faults, overtemperature conditions (Pd) and transient voltage spikes of up to 60V. If the regulator is used in dual supply systems where the load is returned to a negative supply, the output voltage must be diode clamped to ground.

OUTPUT CAPACITOR:

The output voltage ripple of the MSK5130 series voltage regulators can be minimized by placing a filter capacitor from the output to ground. The optimum value for this capacitor may vary from one application to the next, but a minimum of 10µF is recommended for optimum performance. This capacitor need not be an expensive low ESR type: aluminum electrolytics are adequate. In fact, extremely low ESR capacitors may contribute to instability. Tantalum capacitors are recommended for systems where fast load transient response is important. Transient load response can also be improved by placing a capacitor directly across the load.

LOAD CONNECTIONS:

In voltage regulator applications where very large load currents are present, the load connection is very important. The path connecting the output of the regulator to the load must be extremely low impedance to avoid affecting the load regulation specifications. Any impedance in this path will form a voltage divider with the load. The MSK5130 series requires a minimum of 10mA of load current to stay in regulation.

ENABLE PIN:

The MSK5130 series of voltage regulators are equipped with a TTL compatible ENABLE pin. A TTL high level on this pin activates the internal bias circuit and powers up the device. A TTL low level on this pin places the controller in shutdown mode and the device draws approximately 10µA of quiescent current. If the enable function is not used, simply connect the enable pin to the input.

FLAG OUTPUT PIN:

The fixed output voltage version of the MSK5130 series is equipped with a flag output pin. Since the flag pin is an open collector configuration it can be pulled up to any voltage between 3V and 26V. This feature allows direct interfacing to practically any logic. This active low output has a typical level of 0.22V when the flag comparator detects an "out of regulation" condition. Flag states include low input voltage, out of regulation and output current limit. Extremely high level input voltage transients will also cause the flag output pin to activate.

DEVICE/CASE CONNECTION:

The MSK5130 series are highly thermally conductive devices and the thermal path from the package heat sink to the internal junctions is very short. Since the case is electrically isolated from the internal circuitry, the package can be directly connected to a heat sink.

HEAT SINK SELECTION:

To select a heat sink for the MSK 5130, the following formula for convective heat flow may be used.

Governing Equation:

$$T_j = P_d \times (R_{\theta jc} + R_{\theta cs} + R_{\theta sa}) + T_a$$

WHERE:

- T_j = Junction Temperature
- P_d = Total Power Dissipation
- R_{θjc} = Junction to Case Thermal Resistance
- R_{θcs} = Case to Heat Sink Thermal Resistance
- R_{θsa} = Heat Sink to Ambient Thermal Resistance
- T_a = Ambient Temperature

First, the power dissipation must be calculated as follows:

$$\text{Power Dissipation} = (V_{IN} - V_{OUT}) \times I_{out}$$

Next, the user must select a maximum junction temperature. The absolute maximum allowable junction temperature is 125°C. The equation may now be rearranged to solve for the required heat sink to ambient thermal resistance (R_{θsa}).

EXAMPLE:

An MSK5130-3.3 is configured for V_{IN}=+5V and V_{OUT}=+3.3V. I_{out} is a continuous 1A DC level. The ambient temperature is +25°C. The maximum desired junction temperature is 125°C.

$$R_{\theta jc} = 2.4^\circ\text{C/W} \text{ and } R_{\theta cs} = 0.15^\circ\text{C/W} \text{ for most thermal greases}$$

$$\text{Power Dissipation} = (5V - 3.3V) \times (1A)$$

$$= 1.7 \text{ Watts}$$

Solve for R_{θsa}:

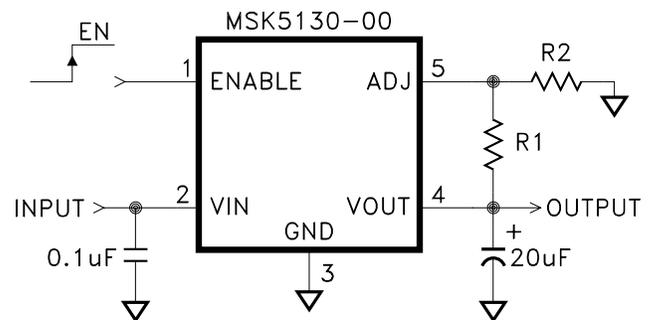
$$R_{\theta sa} = \left[\frac{125^\circ\text{C} - 25^\circ\text{C}}{1.7\text{W}} \right] - 2.4^\circ\text{C/W} - 0.15^\circ\text{C/W}$$

$$= 56.27^\circ\text{C/W}$$

In this example, a heat sink with a thermal resistance of no more than 56°C/W must be used to maintain a junction temperature of no more than 125°C.

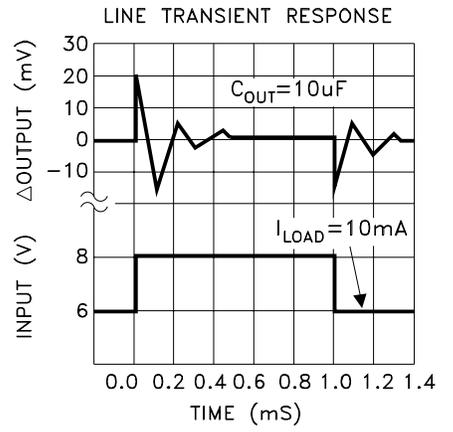
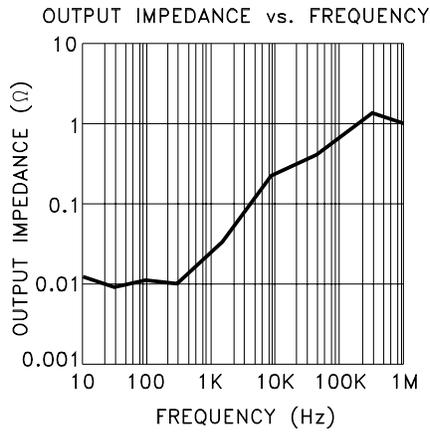
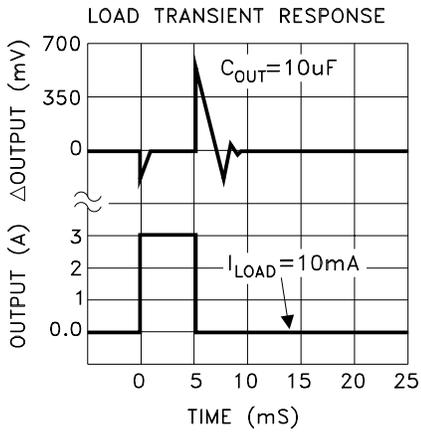
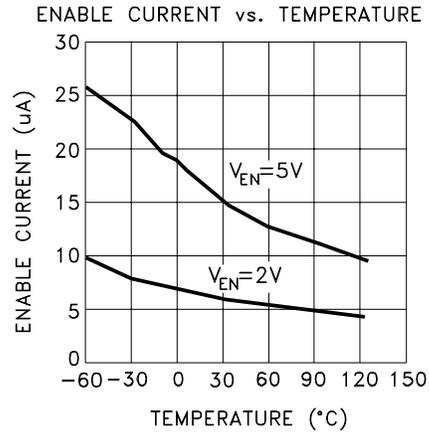
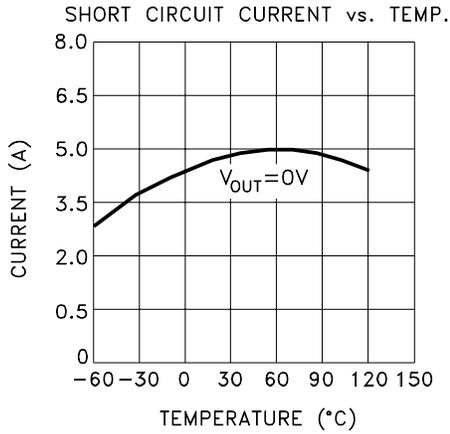
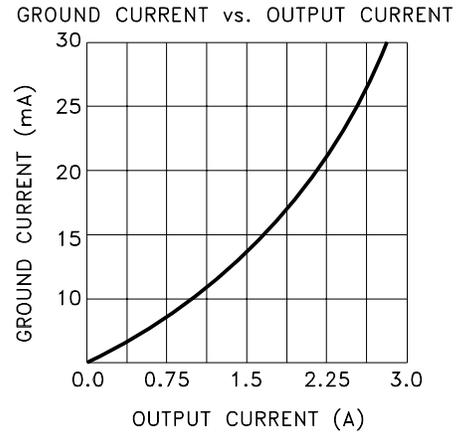
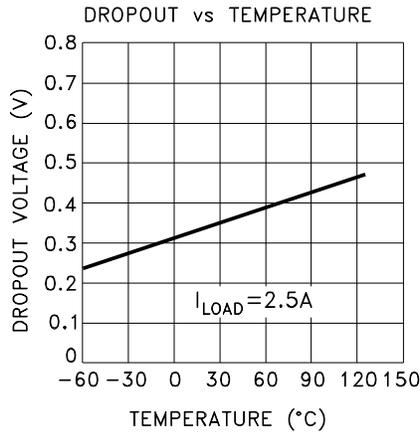
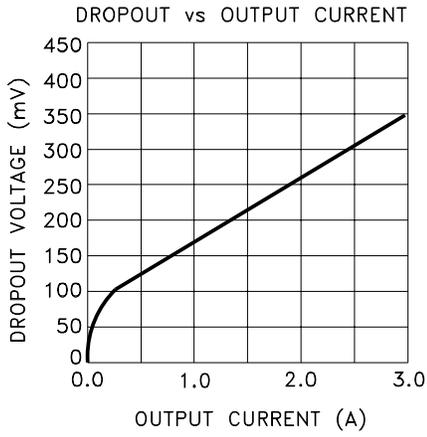
MSK5130-00 OUTPUT ADJUSTMENT:

The MSK5130-00 is an adjustable version in the series of high performance regulators. The diagram below illustrates proper adjustment technique for the output voltage. The series resistance of R1+R2 should be selected to pass the minimum regulator output current requirement of 10mA.

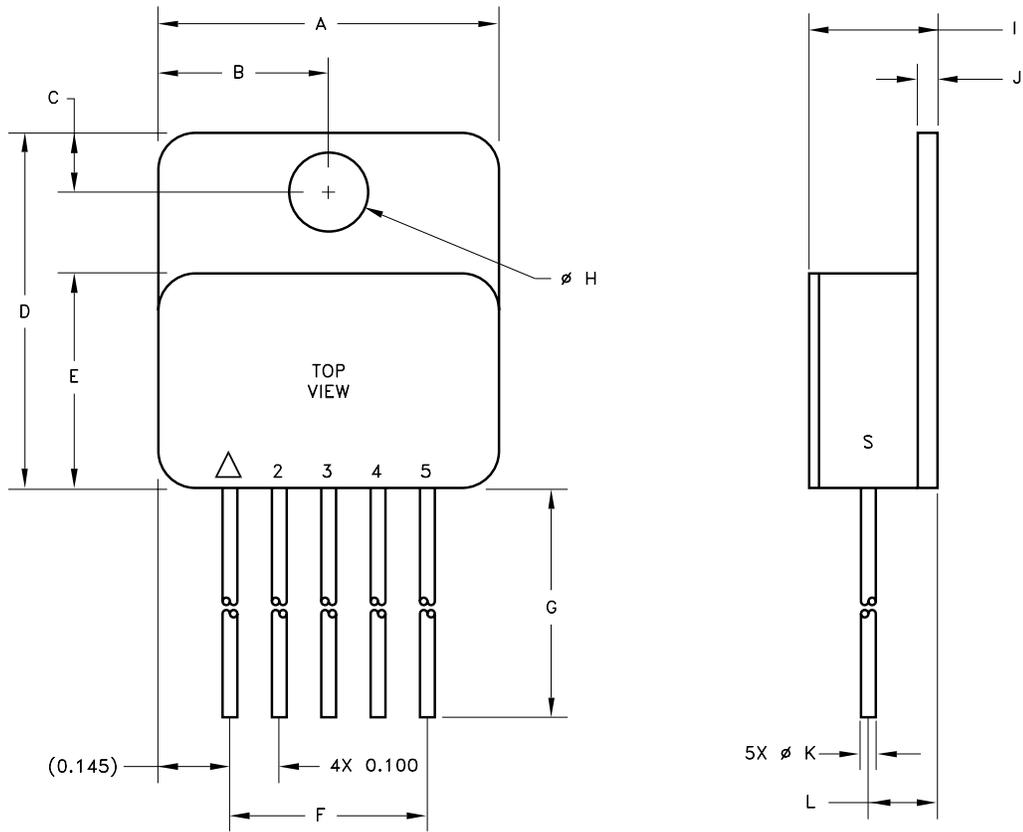


$$V_{OUT} = 1.240V \times \left[1 + \left(\frac{R1}{R2} \right) \right]$$

TYPICAL PERFORMANCE CURVES



MECHANICAL SPECIFICATIONS



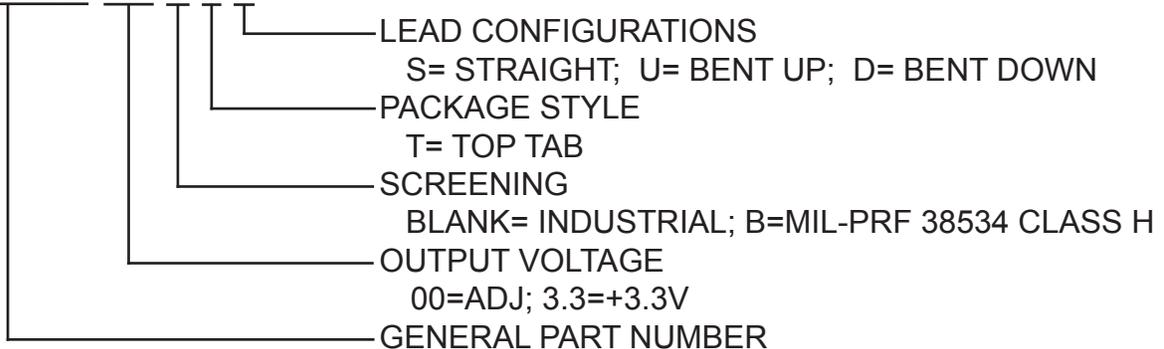
REF	MIN	MAX
A	0.680	0.700
B	0.340	0.350
C	0.113	0.123
D	0.815	0.825
E	0.525	0.545
F	0.395	0.405
G	0.500	
H	0.155	0.165
I		0.255
J	0.035	0.045
K	0.028	0.032
L	0.130	0.150
M	0.185	0.215
N	0.330	

ESD TRIANGLE INDICATES PIN 1
WEIGHT=7.7 GRAMS TYPICAL

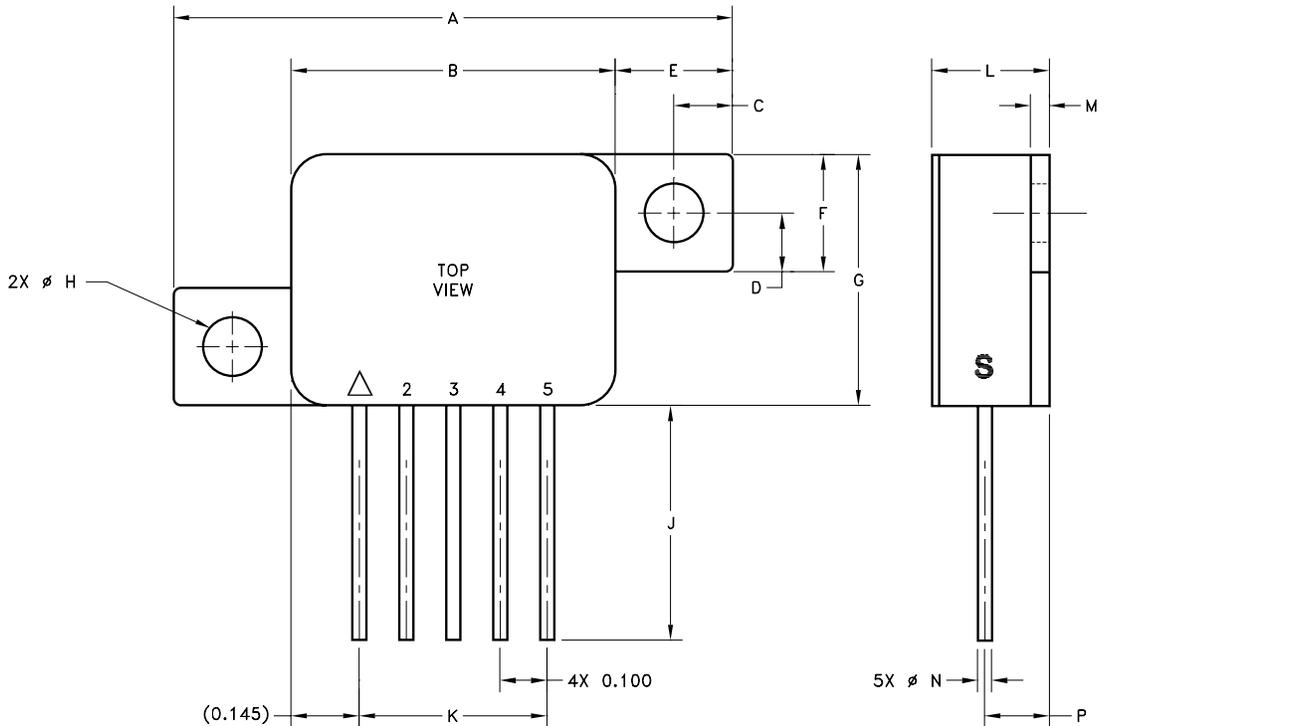
ALL DIMENSIONS ARE SPECIFIED IN INCHES

ORDERING INFORMATION

MSK5130-3.3 B T U



The above example is a +3.3V, Military regulator using the top tab package with leads bent up.



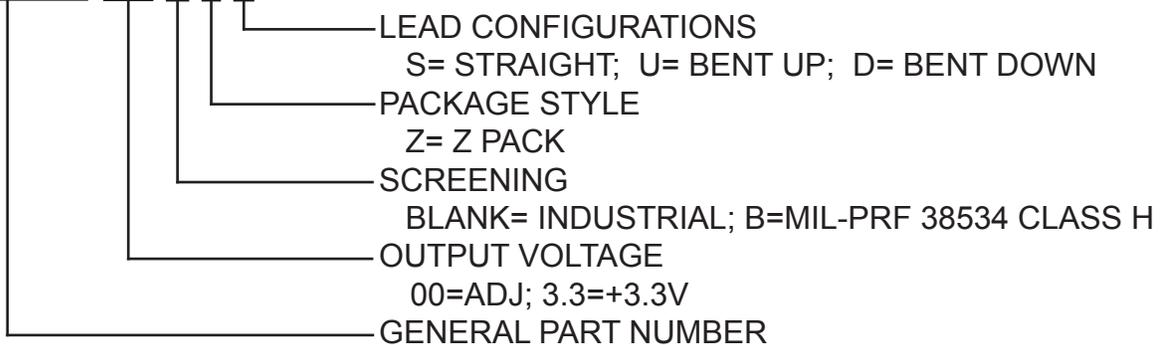
REF	MIN	MAX
A	1.185	1.195
B	0.680	0.700
C	0.120	0.130
D	0.120	0.130
E	0.245	0.255
F	0.245	0.255
G	0.525	0.545
H	0.120	0.130
J	0.500	
K	0.395	0.405
L		0.255
M	0.035	0.045
N	0.028	0.032
P	0.130	0.150
Q	0.185	0.215
R	0.330	

ESD TRIANGLE INDICATES PIN 1
 WEIGHT=7.5 GRAMS TYPICAL

ALL DIMENSIONS ARE SPECIFIED IN INCHES

ORDERING INFORMATION

MSK5130-3.3 B Z U



The above example is a +3.3V, Military regulator using the top tab package with leads bent up.

REVISION HISTORY

REV	STATUS	DATE	DESCRIPTION
J	Released	09/15	Add internal note and clarify mechanical specifications.

MSK
www.anaren.com/msk

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